Amendments to the Claims:

1-11. (Cancelled)

12. (Currently amended) A temperature compensation attenuator comprising

a base **6**;

a film thermistor **1** having two ends, having a top side and a bottom side, and being disposed on said base **6**;

a film resistor **2** having two ends, and having a top side and a bottom side;

an input terminal **3**;

an output terminal **4**; and

a ground terminal **5**;

wherein

said input terminal **3** and said output terminal **4** are connected to said two ends of said film thermistor **1**;

the top side of the film resistor **2** is electronically connected to the bottom side of the film thermistor **1;** ~~and~~

the bottom side of the film resistor **2** is electronically connected to the ground terminal **5**;

the resistance of said film thermistor **1** varies with temperature; and

the resistance of said film resistor **2** is substantially constant over a temperature range.

13. (Currently amended) ~~The attenuator of Claim 12, wherein~~ A temperature compensation attenuator comprising

a base **6**;

a film thermistor **1** having two ends, having a top side and a bottom side, and being disposed on said base **6**;

a film resistor **2** having two ends, and having a top side and a bottom side;

an input terminal **3**;

an output terminal **4**; and

a ground terminal **5**;

wherein

said input terminal **3** and said output terminal **4** are connected to said two ends of said film thermistor **1**;

the top side of the film resistor **2** is electronically connected to the bottom side of the film thermistor **1**;

the bottom side of the film resistor **2** is electronically connected to the ground terminal **5**; and

said two ends of said film resistor **2** are connected to the input terminal **3** and the output terminal **4**, respectively.

14. (Previously presented) The attenuator of Claim 12, wherein said film resistor **2** is a film thermistor having a temperature characteristic opposite to that of the film thermistor **1**.

15. (Previously presented) The attenuator of Claim 13, wherein said film resistor **2** is a film thermistor having a temperature characteristic opposite to that of the film thermistor **1**.

16. (Previously presented) The attenuator of Claim 14, wherein said film thermistor **1** has a negative temperature coefficient, and said film resistor **2** has a positive temperature coefficient.

17. (Previously presented) The attenuator of Claim 14, wherein said film thermistor **1** has a positive temperature coefficient, and said film resistor **2** has a negative temperature coefficient.

18. (Previously presented) The attenuator according to claim 14, wherein the resistance value and the temperature coefficient of said film thermistor **1** and said film resistor **2** are selected in accordance with the compensation of the gain and the power level in order to satisfy the requirement for the size of the attenuation, isolation, and reflection coefficients.

19. (Previously presented) The attenuator of Claim 12, wherein said film thermistor **1** and said film resistor **2** are configured in series, in parallel, or in combination.

20. (Previously presented) The attenuator of Claim 13, wherein said film thermistor **1** and said film resistor **2** are configured in series, in parallel, or in combination.

21. (Previously presented) The attenuator of claim 12, wherein said film thermistor **1** having multiple sides contacts with said film resistor **2** having multiple sides in one of the following manners: one side of said film thermistor **1** is electronically contacted with multiple sides of the film resistor **2**, multiple sides of said film thermistor **1** are electronically contacted with one side of said film resistor **2**, or multiple sides of said film thermistor **1** are electronically contacted with multiple sides of said film resistor **2**.

22. (Previously presented) The attenuator of claim 13, wherein said film thermistor **1** having multiple sides contacts with said film resistor **2** having multiple sides in one of the following manners: one side of said film thermistor **1** is electronically contacted with multiple sides of the film resistor **2**, multiple sides of said film thermistor **1** are electronically contacted with one side of said film resistor **2**, or multiple sides of said film thermistor **1** are electronically contacted with multiple sides of said film resistor **2**.

23. (Previously presented) The attenuator of Claim 21, wherein said film thermistor **1**, said film resistor **2**, said input terminal **3**, said output terminal **4**, and said ground terminal **5** are disposed in the same plane or in different planes.

24. (Previously presented) The attenuator of Claim 22, wherein said film thermistor **1**, said film resistor **2**, said input terminal **3**, said output terminal **4**, and said ground terminal **5** are disposed in the same plane or in different planes.

25. (Previously presented) The attenuator of claim 12, wherein the configuration of said attenuator is one of a surface mount type, a pin leg lead type, or a patch cord type.

26. (Previously presented) The attenuator of claim 13, wherein the configuration of said attenuator is one of a surface mount type, a pin leg lead type, or a patch cord type.

27. (Previously presented) The attenuator of claim 12, wherein said attenuator is integrated on the base **6** by printing the film thermistor using multilayer masking technology.

28. (Previously presented) The attenuator of claim 13, wherein said attenuator is integrated on the base **6** by printing the film thermistor using multilayer masking technology.

29. (New) A temperature compensation attenuator comprising

a base **6**;

a film thermistor **1** having two ends, having a top side and a bottom side, and being disposed on said base **6**;

a film resistor **2** having two ends, and having a top side and a bottom side;

an input terminal **3**;

an output terminal **4**; and

a ground terminal **5**;

wherein

said input terminal **3** and said output terminal **4** are connected to said two ends of said film thermistor **1**;

the top side of the film resistor **2** is electronically connected to the bottom side of the film thermistor **1**;

the bottom side of the film resistor **2** is electronically connected to the ground terminal **5**; and

said film resistor **2** is not a thermistor.